

## Refine Search

### Search Results -

| Terms                    | Documents |
|--------------------------|-----------|
| semiconductor and finfet | 0         |

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
**JPO Abstracts Database**  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L6





### Search History

**DATE:** Friday, September 24, 2004    [Printable Copy](#)    [Create Case](#)

**Set Name Query**

side by side

**Hit Count Set Name**

result set

*DB=JPAB; PLUR=YES; OP=ADJ*

|                                    |   |           |
|------------------------------------|---|-----------|
| <u>L6</u> semiconductor and finfet | 0 | <u>L6</u> |
|------------------------------------|---|-----------|

*DB=EPAB; PLUR=YES; OP=ADJ*

|                                    |   |           |
|------------------------------------|---|-----------|
| <u>L5</u> semiconductor and finfet | 5 | <u>L5</u> |
|------------------------------------|---|-----------|

*DB=USPT; PLUR=YES; OP=ADJ*

|                                    |    |           |
|------------------------------------|----|-----------|
| <u>L4</u> semiconductor and finfet | 63 | <u>L4</u> |
|------------------------------------|----|-----------|

*DB=PGPB; PLUR=YES; OP=ADJ*

|                                    |    |           |
|------------------------------------|----|-----------|
| <u>L3</u> semiconductor and finfet | 50 | <u>L3</u> |
|------------------------------------|----|-----------|

*DB=TDBD; PLUR=YES; OP=ADJ*

|                                    |   |           |
|------------------------------------|---|-----------|
| <u>L2</u> semiconductor and finfet | 0 | <u>L2</u> |
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*DB=DWPI; PLUR=YES; OP=ADJ*

|                                    |    |           |
|------------------------------------|----|-----------|
| <u>L1</u> semiconductor and finfet | 20 | <u>L1</u> |
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END OF SEARCH HISTORY